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By James R. Armstrong

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<http://www.lehmanns.de/shop/mathematik-informatik/2165316-9780130216700-structured-logic-design-with-vhdl>

Structured Logic Design with VHDL Prentice Hall Modern Semiconductor Design Series'sub Series: James R. Armstrong, authored Chip Level Modeling With VHDL,

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(VHSIC Hardware Description Language) A comparison of the coding styles between the RTL modeling and Algorithm level Modeling a real industry chip

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<http://www.ece.vt.edu/faculty/armstrong.php>

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Find Booking Information on Author James R. Armstrong such as Biography, Upcoming Author Appearances, Speaking Engagements,

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The IEEE Standard 1076 defines the VHSIC Hardware Description Language or when a VHDL model is translated into the VHDL for use with their chips,

http://en.wikipedia.org/wiki/VHSIC_hardware_description_language

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Chip-level modeling with VHDL. Author: James R. Armstrong: Virginia Polytechnic Institute and State Univ. Publication: Book: Chip-level modeling with VHDL :

<http://dl.acm.org/citation.cfm?id=49146&prelayout=tabs>

James R. Armstrong, Chip-level modeling with VHDL, Prentice-Hall, Inc., Upper Saddle River, NJ, 1988 .

Generating VHDL models from natural language descriptions:

<http://dl.acm.org/citation.cfm?id=198344&dl=ACM&coll=DL>

New and Collectible Books available now at AbeBooks.co.uk. A Guide to VHDL. Stanley Mazor and Patricia Langstraat. Published by Springer (1993)

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Works by James R. Armstrong: Chip-level modeling with VHDL, Structured logic design with VHDL, VHDL Design Representation and Synthesis (2nd Edition)

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He was a member of the original IEEE standardization committee; authored Chip Level Modeling With VHDL, By James R. Armstrong, F. Gail Gray;

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<http://www.sciencedirect.com/science/article/pii/016560749390197S>

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<http://dl.acm.org/citation.cfm?id=645332.649830>

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http://link.springer.com/chapter/10.1007/978-94-011-1914-6_10

Making Verilog models compatible with VHDL VITAL level 0 models by Yuri Tatarnikov, SEVA Technologies modeling PCB interconnect delays as chip port delays.

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<https://www.annapmicro.com/products/vhdl/>

James R. Armstrong is the author of VHDL Design Representation and Synthesis (4.50 avg rating, 4 ratings, 0 reviews, published 2000), Chip Level Modeling
http://www.goodreads.com/author/show/677325.James_R_Armstrong

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